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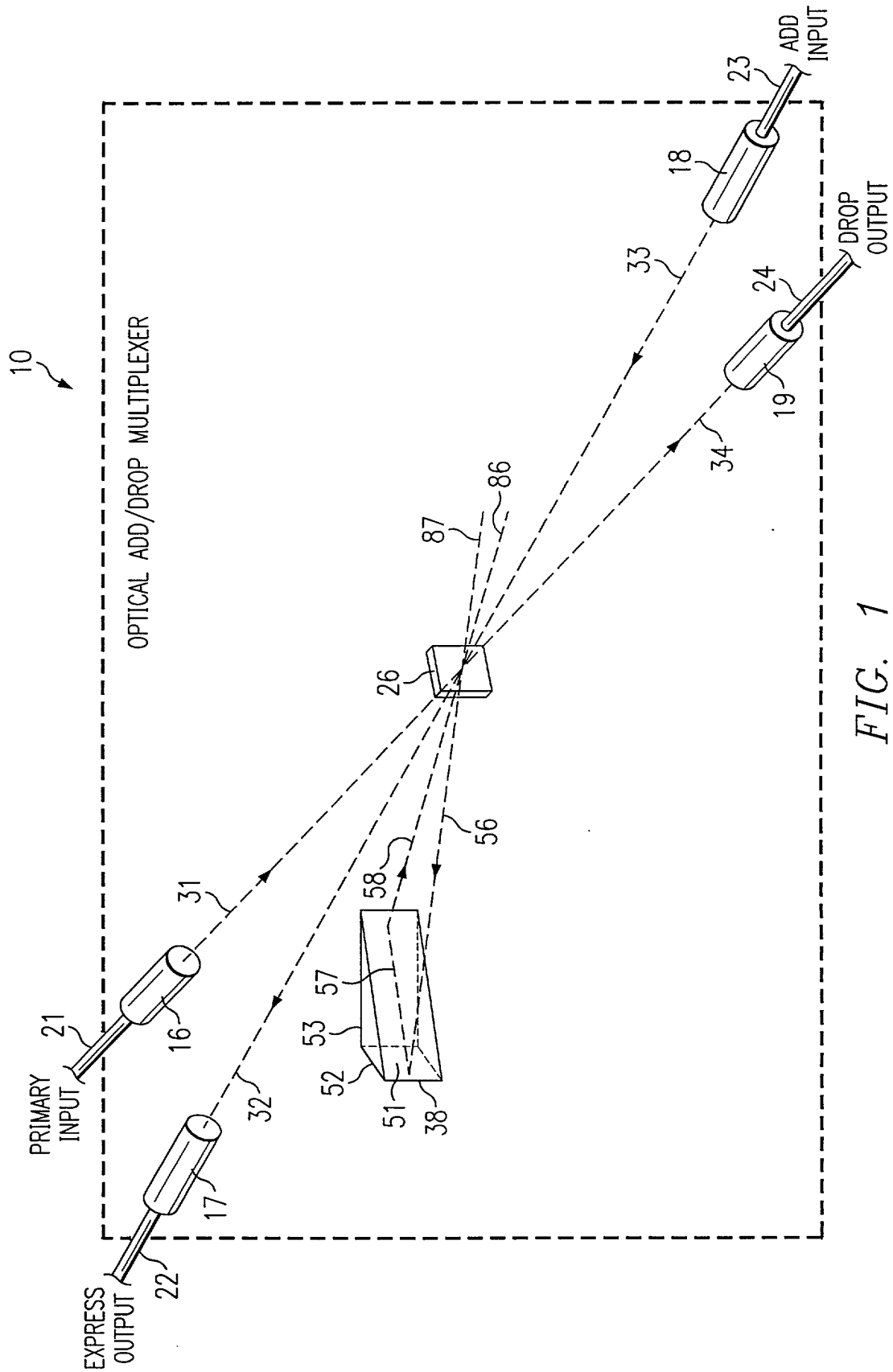


FIG. 1

FIG. 1 is a schematic diagram of an optical add/drop multiplexer (10) in accordance with the present invention.

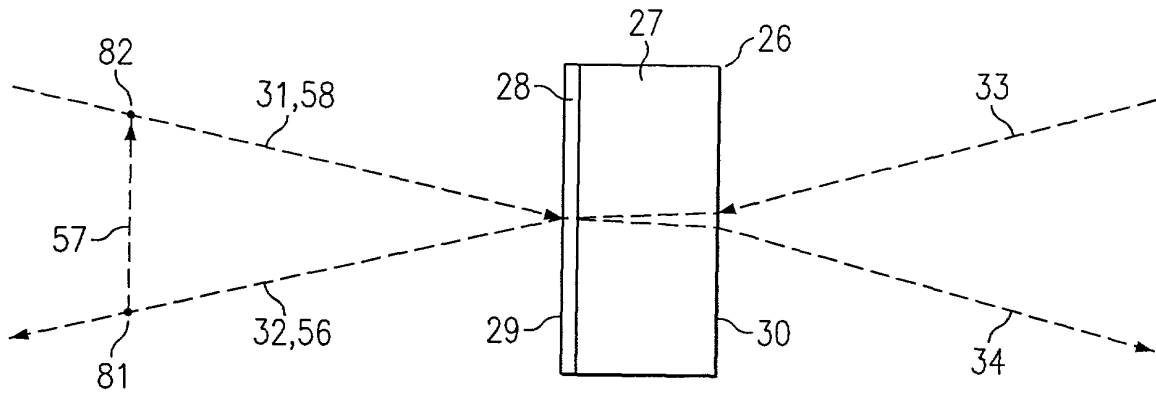
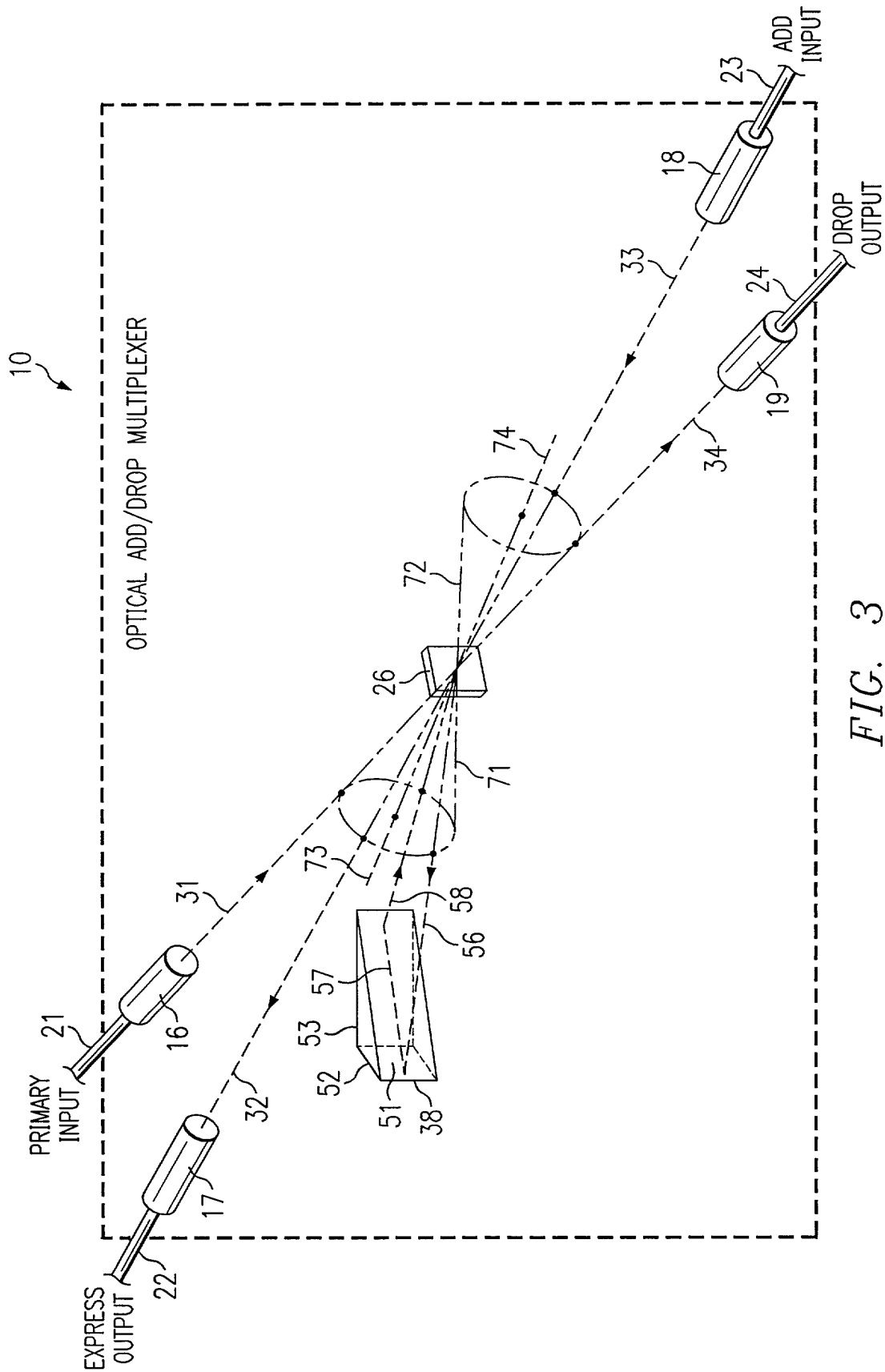


FIG. 2



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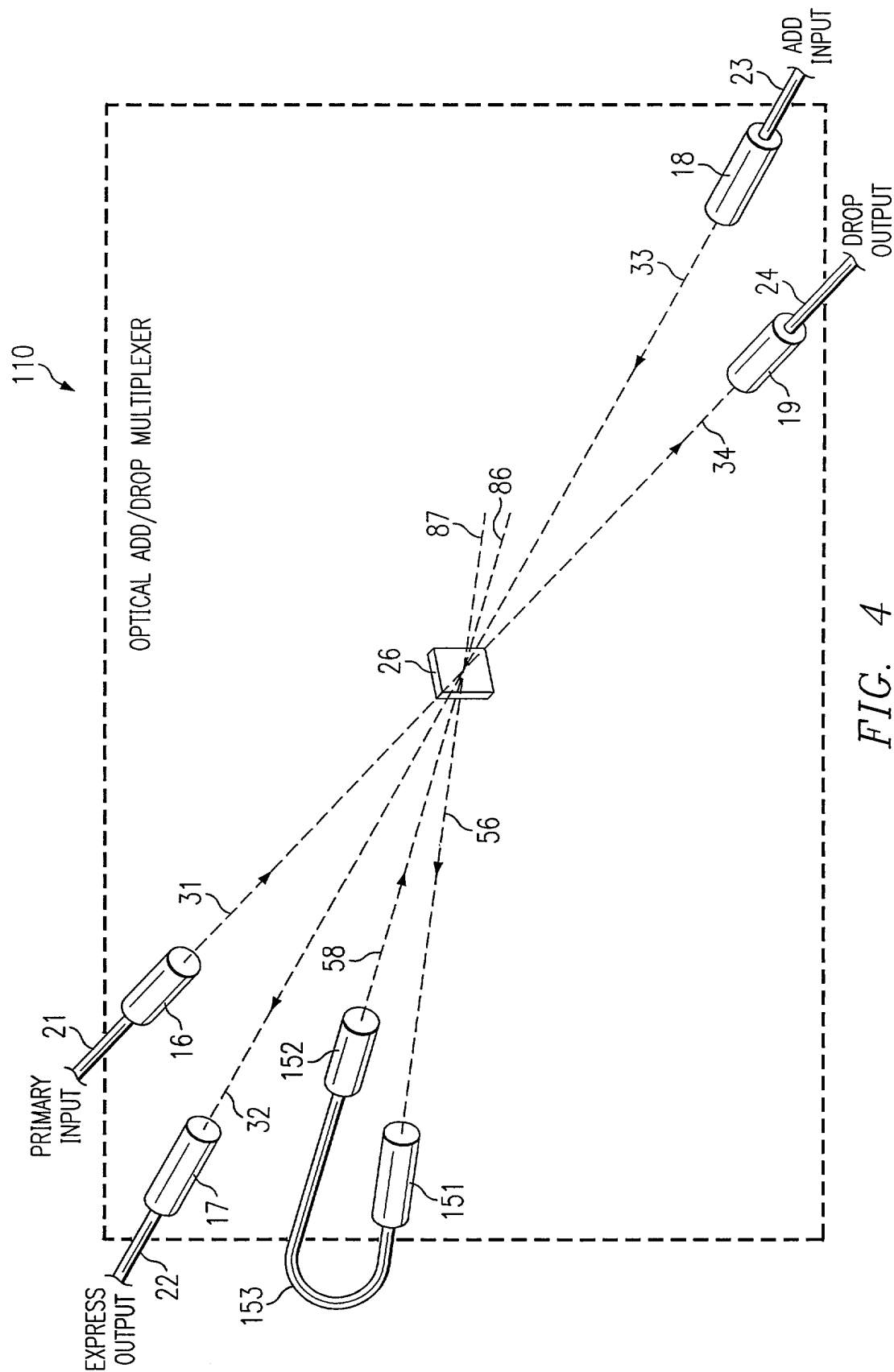


FIG. 4

FIG. 4

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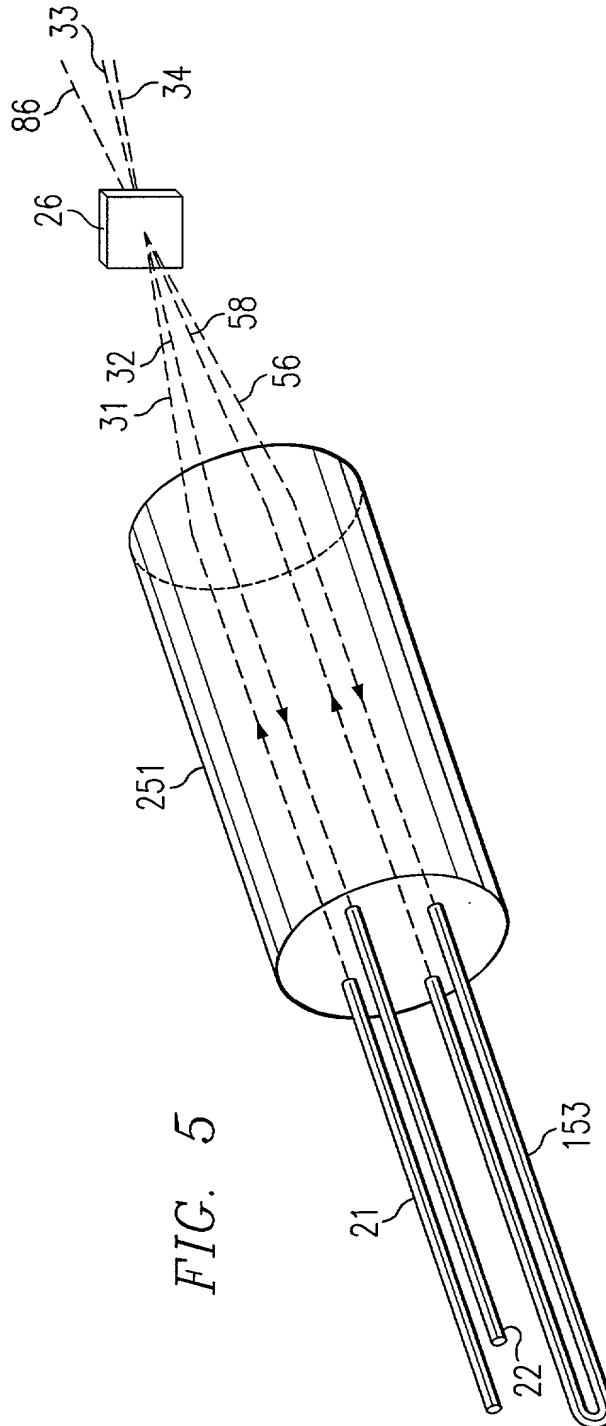


FIG. 5